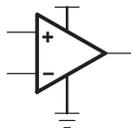


FAMILY OF LOW-POWER WIDE BANDWIDTH SINGLE SUPPLY OPERATIONAL AMPLIFIERS WITH AND WITHOUT SHUTDOWN

FEATURES

- Rail-To-Rail Output
- V_{ICR} Includes Ground
- Gain-Bandwidth Product . . . 9 MHz
- Supply Current . . . 730 $\mu\text{A}/\text{Channel}$
- Single, Duals, and Quad Versions
- Ultralow Power Down Mode
 $I_{DD}(\text{SHDN}) = 4 \mu\text{A}/\text{Channel}$
- Specified Temperature Range
 -40°C to 125°C . . . Industrial Grade
- Supply Voltage Range . . . 2.7 V to 5.5 V
- Ultrasmall Packaging
5 or 6 Pin SOT-23 (TLV2630/1)
8 or 10 Pin MSOP (TLV2632/3)
- Universal Op-Amp EVM (See SLOU060
for More Information)

Operational Amplifier



DESCRIPTION

The TLV263x single supply operational amplifiers provide rail-to-rail output with an input range that includes ground. The TLV263x takes the minimum operating supply voltage down to 2.7 V over the extended industrial temperature range (-40°C to 125°C) while adding the rail-to-rail output swing feature. The TLV263x also provides a 9 MHz gain-bandwidth product from only 730 μA of supply current. The maximum recommended supply voltage is 5.5 V, which, when coupled with a 2.7-V minimum, allows the devices to be operated from lithium ion cells.

The combination of wide bandwidth, low noise, and low distortion makes it ideal for high speed and high resolution data converter applications. The ground input range allows it to directly interface to ground rail referred systems.

All members are available in PDIP and SOIC with the singles in the small SOT-23 package, duals in the MSOP, and quads in the TSSOP package.

The 2.7-V operation makes it compatible with Li-Ion powered systems and the operating supply voltage range of many micro-power microcontrollers available today including TI's MSP430.

AMPLIFIER SELECTION TABLE

DEVICE	V_{DD} [V]	I_{DD}/ch [μA]	V_{ICR} [V]	GBW [MHz]	SLEW RATE [V/ μs]	V_n , 1 kHz [nV/ $\sqrt{\text{Hz}}$]	I_O [mA]
OPAx343	2.5–5.5	850	-0.3 to $V_{DD} + 0.3$	5.5	6	25	40
OPAx743	3.5–12	1100	-0.1 to $V_{DD} + 0.1$	7	10	30	20
TLV278x	1.8–3.6	650	-0.2 to $V_{DD} + 0.2$	8	5	9	10
TLV263x	2.7–5.5	730	GND to $V_{DD} - 1$	9	9.5	50	28
TLV262x	2.7–5.5	750	1 V to $V_{DD} + 0.2$	11	10	27	28
OPAx353	2.7–5.5	8000	-0.1 to $V_{DD} + 0.1$	44	22	7	40



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

TLV2630, TLV2631
TLV2632, TLV2633
TLV2634, TLV2635

SLOS362A – JUNE 2001 – REVISED JANUARY 2005

PACKAGE/ORDERING INFORMATION(1)

PRODUCT	PACKAGE	PACKAGE CODE	SYMBOL	SPECIFIED TEMPERATURE RANGE	ORDER NUMBER	TRANSPORT MEDIA
Single with Shutdown						
TLV2630ID	SOIC-8	D	—	-40°C to 125°C	TLV2630ID TLV2630IDR	Tube Tape and Reel
TLV2630IDBV	SOT-23-6	DBV	VAYI		TLV2630IDBVR† TLV2630IDBVT‡	Tape and Reel
TLV2630IP	DIP-8	P	—		TLV2630IP	Tube
Single without Shutdown						
TLV2631ID	SOIC-8	D	—	-40°C to 125°C	TLV2631ID TLV2631IDR	Tube Tape and Reel
TLV2631IDBV	SOT-23-5	DBV	VAZI		TLV2631IDBVR† TLV2631IDBVT‡	Tape and Reel
TLV2631IP	DIP-8	P	—		TLV2631IP	Tube
Dual without Shutdown						
TLV2632ID	SOIC-8	D	—	-40°C to 125°C	TLV2632ID TLV2632IDR	Tube Tape and Reel
TLV2632IDGK	MSOP-8	DGK	AKG		TLV2632IDGK TLV2632IDGKR	Tube Tape and Reel
TLV2632IP	DIP-8	P	—		TLV2632IP	Tube
Dual with Shutdown						
TLV2633ID	SOIC-14	D	—	-40°C to 125°C	TLV2633ID TLV2633IDR	Tube Tape and Reel
TLV2633IDGS	MSOP-10	DGS	AKK		TLV2633IDGS TLV2633IDGSR	Tube Tape and Reel
TLV2633IN	DIP-14	N	—		TLV2633IN	Tube
Quad without Shutdown						
TLV2634ID	SOIC-14	D	—	-40°C to 125°C	TLV2634ID TLV2634IDR	Tube Tape and Reel
TLV2634IN	DIP-14	N	—		TLV2634IN	Tube
TLV2634IPW	TSSOP-14	PW	—		TLV2634IPW TLV2634IPWR	Tube Tape and Reel
Quad with Shutdown						
TLV2635ID	SOIC-16	D	—	-40°C to 125°C	TLV2635ID TLV2635IDR	Tube Tape and Reel
TLV2635IN	DIP-16	N	—		TLV2635IN	Tube
TLV2635IPW	TSSOP-16	PW	—		TLV2635IPW TLV2635IPWR	Tube Tape and Reel

† The SOT23 package devices are only available taped and reeled. The R Suffix denotes quantities (3,000 pieces per reel).

‡ The T Suffix denotes smaller quantities (250 pieces per mini-reel).

1. For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V_{DD} (see Note 1)	6 V
Differential input voltage, V_{ID}	$\pm V_{DD}$
Input voltage range, V_I (see Note 1)	GND to $V_{DD} - 1$ V
Input current, I_I (any input)	± 10 mA
Output current, I_O	± 40 mA
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T_A : I-suffix	-40°C to 125°C
Maximum junction temperature, T_J	150°C
Storage temperature range, T_{stg}	-65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

† Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 2: All voltage values, except differential voltages, are with respect to GND.

recommended operating conditions

		MIN	MAX	UNIT
Supply voltage, V_{DD}	Single supply	2.7	5.5	V
	Split supply	± 1.35	± 2.75	
Common-mode input voltage range, V_{ICR}		GND	$V_{DD}-1$	V
Operating free-air temperature, T_A	I-suffix	-40	125	$^\circ\text{C}$
Shutdown on/off voltage level‡	V_{IL}		0.4	V
	V_{IH}	2		

‡ Relative to GND.

electrical characteristics at specified free-air temperature, $V_{DD} = 2.7$ V, 5 V (unless otherwise noted)

dc performance

PARAMETER		TEST CONDITIONS		T_A	MIN	TYP	MAX	UNIT
V_{IO} Input offset voltage		$V_{IC} = V_{DD}/2,$ $V_O = V_{DD}/2$	TLV2634/5	25 $^\circ\text{C}$	250	3500	μV	
				Full range	4500			
				25 $^\circ\text{C}$	250	4200	μV	
				Full range	5200			
α_{VIO} Temperature coefficient of input offset voltage				25 $^\circ\text{C}$	3	$\mu\text{V}/^\circ\text{C}$		
CMRR Common-mode rejection ratio		$V_{IC} = \text{GND to } V_{DD}-1$ V	$V_{DD} = 2.7$ V	25 $^\circ\text{C}$	76	100	dB	
				Full range	67			
			$V_{DD} = 5$ V	25 $^\circ\text{C}$	77	100		
				Full range	74			
A_{VD} Large-signal differential voltage amplification		$R_L = 2$ k Ω , $V_{O(PP)} = V_{DD}-1$ V		25 $^\circ\text{C}$	90	100	dB	
				Full range	82			

electrical characteristics at specified free-air temperature, $V_{DD} = 2.7\text{ V}, 5\text{ V}$ (unless otherwise noted) (continued)

input characteristics

PARAMETER	TEST CONDITIONS	T_A^\dagger	MIN	TYP	MAX	UNIT
I_{IO} Input offset current	$V_{IC} = V_{DD}/2,$ $V_O = V_{DD}/2$	25°C		1	50	pA
		Full range			100	
25°C			1	50		
		Full range			200	
$r_{i(d)}$ Differential input resistance		25°C		1000		GΩ
$C_{i(c)}$ Common-mode input capacitance	$f = 1\text{ kHz}$	25°C		12		pF

† Full range is –40°C to 125°C for the I-suffix.

output characteristics

PARAMETER	TEST CONDITIONS	T_A^\dagger	MIN	TYP	MAX	UNIT	
V_{OH} High-level output voltage	$V_{IC} = V_{DD}/2, I_{OH} = -1\text{ mA}$	$V_{DD} = 2.7\text{ V}$	25°C	2.6	2.67	V	
			Full range		2.55		
		$V_{DD} = 5\text{ V}$	25°C	4.92	4.98		
			Full range		4.9		
	$V_{IC} = V_{DD}/2, I_{OH} = -10\text{ mA}$	$V_{DD} = 2.7\text{ V}$	25°C	2.25	2.43		
			Full range		2.15		
		$V_{DD} = 5\text{ V}$	25°C	4.7	4.8		
			Full range		4.65		
V_{OL} Low-level output voltage	$V_{IC} = V_{DD}/2, I_{OL} = 1\text{ mA}$	$V_{DD} = 2.7\text{ V}$	25°C		0.03	0.1	mV
			Full range			0.15	
		$V_{DD} = 5\text{ V}$	25°C		0.025	0.08	
			Full range			0.1	
	$V_{IC} = V_{DD}/2, I_{OL} = 10\text{ mA}$	$V_{DD} = 2.7\text{ V}$	25°C		0.26	0.45	
			Full range			0.47	
		$V_{DD} = 5\text{ V}$	25°C		0.2	0.3	
			Full range			0.35	
I_O Output current	$V_{DD} = 2.7\text{ V},$ $V_O = 0.5\text{ V}$ from rail	Sourcing	25°C		14	mA	
		Sinking			19		
	$V_{DD} = 5\text{ V},$ $V_O = 0.5\text{ V}$ from rail	Sourcing			28		
		Sinking			28		
I_{OS} Short-circuit output current	Sourcing	$V_{DD} = 2.7\text{ V}$	25°C		50	mA	
		$V_{DD} = 5\text{ V}$			95		
	Sinking	$V_{DD} = 2.7\text{ V}$			50		
		$V_{DD} = 5\text{ V}$			95		

† Full range is –40°C to 125°C for the I-suffix.

power supply

PARAMETER	TEST CONDITIONS	T_A^\dagger	MIN	TYP	MAX	UNIT	
I_{DD} Supply current (per channel)	$V_O = V_{DD}/2,$ $\overline{\text{SHDN}} = V_{DD}$	25°C		730	1000	μA	
		Full range			1350		
PSRR Supply voltage rejection ratio ($\Delta V_{DD} / \Delta V_{IO}$)	$V_{DD} = 2.7\text{ V}$ to $5.5\text{ V},$ $V_{IC} = V_{DD}/2$	No load	25°C		70	90	dB
				Full range		65	

† Full range is –40°C to 125°C for the I-suffix.

electrical characteristics at specified free-air temperature, $V_{DD} = 2.7\text{ V}, 5\text{ V}$ (unless otherwise noted)
 (continued)

dynamic performance

PARAMETER		TEST CONDITIONS		T_A^\dagger	MIN	TYP	MAX	UNIT
GBWP	Gain-bandwidth product	$R_L = 2\text{ k}\Omega, C_L = 10\text{ pF}, f = 10\text{ kHz}$		25°C		9		MHz
SR+	Positive slew rate at unity gain	$R_L = 2\text{ k}\Omega, C_L = 50\text{ pF}$	$V_{DD} = 2.7\text{ V}, V_{O(PP)} = 1.7\text{ V}$			6		V/ μs
			$V_{DD} = 5\text{ V}, V_{O(PP)} = 3.5\text{ V}$			6		V/ μs
SR-	Negative slew rate at unity gain	$R_L = 2\text{ k}\Omega, C_L = 50\text{ pF}$	$V_{DD} = 2.7\text{ V}, V_{O(PP)} = 1.7\text{ V}$			10		V/ μs
			$V_{DD} = 5\text{ V}, V_{O(PP)} = 3.5\text{ V}$			9.5		V/ μs
ϕ_m	Phase margin	$R_L = 2\text{ k}\Omega, C_L = 10\text{ pF}$				50		°
	Gain margin				20		dB	

† Full range is -40°C to 125°C for the I-suffix.

noise/distortion performance

PARAMETER		TEST CONDITIONS		T_A	MIN	TYP	MAX	UNIT	
THD + N	Total harmonic distortion plus noise	$V_{O(PP)} = V_{DD}/2, R_L = 2\text{ k}\Omega, f = 10\text{ kHz}$	$A_V = 1$	25°C		0.003%			
			$A_V = 10$			0.02%			
			$A_V = 100$			0.095%			
V_n	Equivalent input noise voltage	$f = 1\text{ kHz}$					50		nV/ $\sqrt{\text{Hz}}$
		$f = 10\text{ kHz}$					30		nV/ $\sqrt{\text{Hz}}$
I_n	Equivalent input noise current	$f = 1\text{ kHz}$					0.9		fA/ $\sqrt{\text{Hz}}$

shutdown characteristics

PARAMETER		TEST CONDITIONS		T_A^\dagger	MIN	TYP	MAX	UNIT
$I_{DD(SHDN)}$	Supply current, per channel in shutdown mode (TLV2630, TLV2633, TLV2635)	$\overline{\text{SHDN}} = 0.4\text{ V}$		25°C		4	17	μA
				Full range			19	μA
$t_{(on)}$	Amplifier turnon time ‡	$R_L = 2\text{ k}\Omega, C_L = 10\text{ pF}$	$V_{DD} = 2.7\text{ V}$	25°C		4.5		μs
			$V_{DD} = 5\text{ V}$			1.5		μs
$t_{(off)}$	Amplifier turnoff time ‡						200	

† Full range is -40°C to 125°C for the I-suffix.

‡ Disable time and enable time are defined as the interval between application of the logic signal to $\overline{\text{SHDN}}$ and the point at which the supply current has reached half its final value.

DISSIPATION RATING TABLE

PACKAGE	Θ_{JC} (°C/W)	Θ_{JA} (°C/W)	$T_A \leq 25^\circ\text{C}$ POWER RATING	$T_A = 125^\circ\text{C}$ POWER RATING
D (8)	38.3	176	710 mW	142 mW
D (14)	26.9	122.3	1022 mW	204.4 mW
D (16)	25.7	114.7	1090 mW	218 mW
DBV (5)	55	324.1	385 mW	77.1 mW
DBV (6)	55	294.3	425 mW	85 mW
DGK (8)	54.2	259.9	481 mW	96.1 mW
DGS (10)	54.1	259.7	485 mW	97 mW
N (14, 16)	32	78	1600 mW	320.5 mW
P (8)	41	104	1200 mW	240.4 mW
PW (14)	29.3	173.6	720 mW	144 mW
PW (16)	28.7	161.4	774 mW	154.9 mW

TYPICAL CHARACTERISTICS

Table of Graphs

			FIGURE
V_{IO}	Input offset voltage	vs Common-mode input voltage	1, 2
CMRR	Common-mode rejection ratio	vs Frequency	3
V_{OH}	High-level output voltage	vs High-level output current	4, 6
V_{OL}	Low-level output voltage	vs Low-level output current	5, 7
I_{DD}	Supply current	vs Supply voltage	8
I_{DD}	Supply current	vs Free-air temperature	9
PSRR	Power supply rejection ratio	vs Frequency	10
A_{VD}	Differential voltage amplification & phase	vs Frequency	11
	Gain-bandwidth product	vs Supply voltage	12
		vs Free-air temperature	13
SR	Slew rate	vs Supply voltage	14
		vs Free-air temperature	15, 16
ϕ_m	Phase margin	vs Load capacitance	17
V_n	Equivalent input noise voltage	vs Frequency	18
	Crosstalk	vs Frequency	19
	Voltage-follower large-signal pulse response		20
	Voltage-follower small-signal pulse response		21
$I_{DD(SHDN)}$	Shutdown supply current	vs Free-air temperature	22
$I_{DD(SHDN)}$	Shutdown supply current	vs Supply voltage	23
$I_{DD(SHDN)}$	Shutdown supply current/output voltage	vs Time	24

TYPICAL CHARACTERISTICS

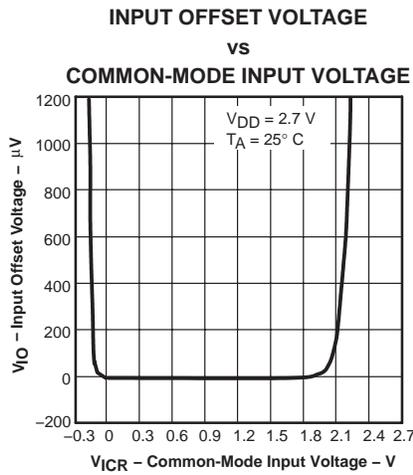


Figure 1

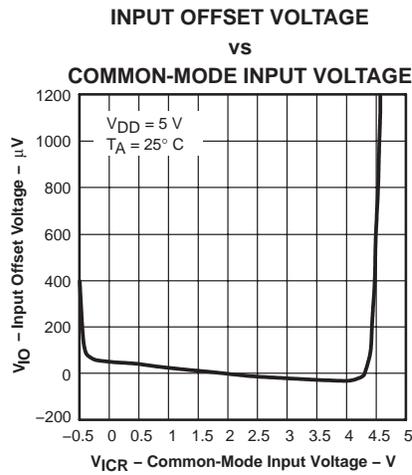


Figure 2

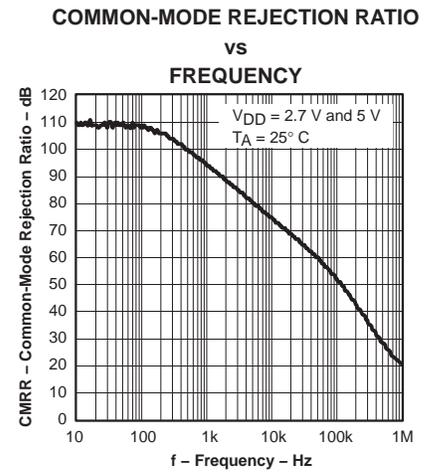


Figure 3

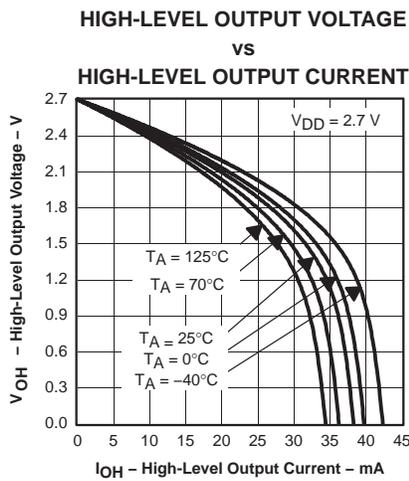


Figure 4

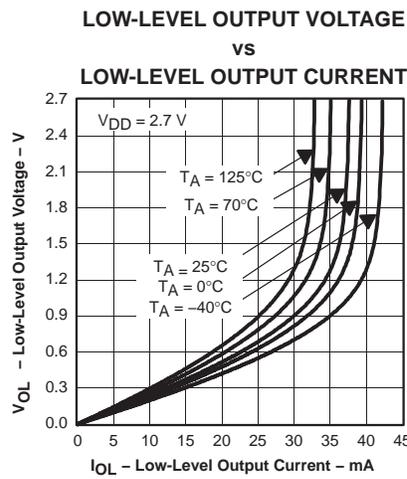


Figure 5

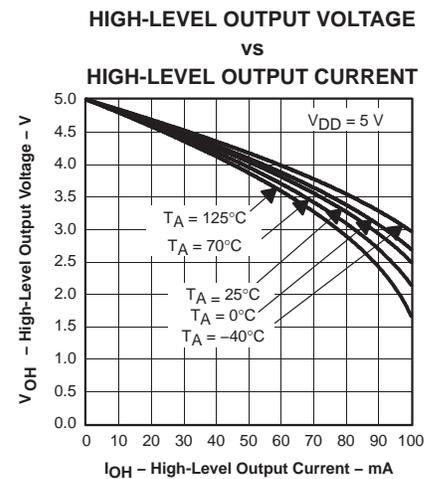


Figure 6

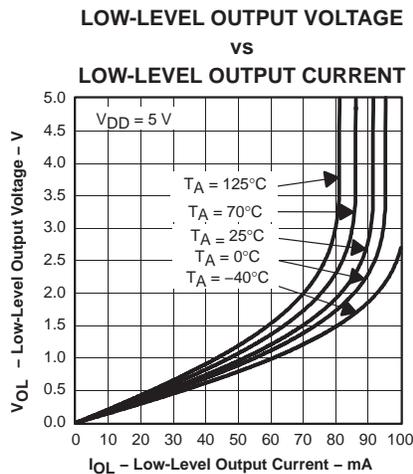


Figure 7

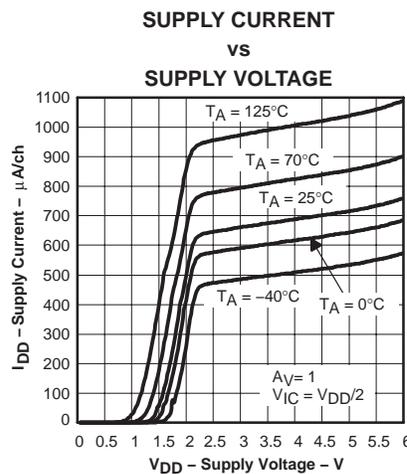


Figure 8

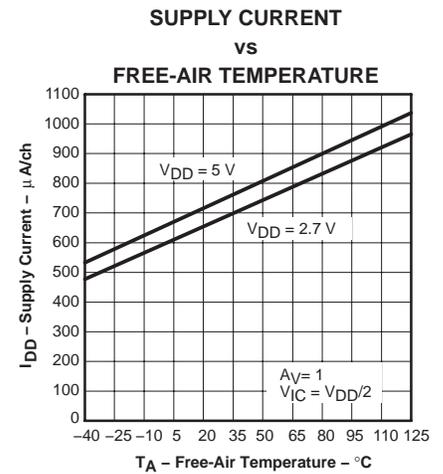


Figure 9

TYPICAL CHARACTERISTICS

POWER SUPPLY REJECTION RATIO

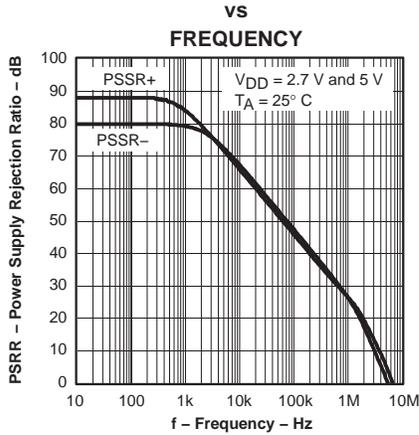


Figure 10

DIFFERENTIAL VOLTAGE AMPLIFICATION AND PHASE

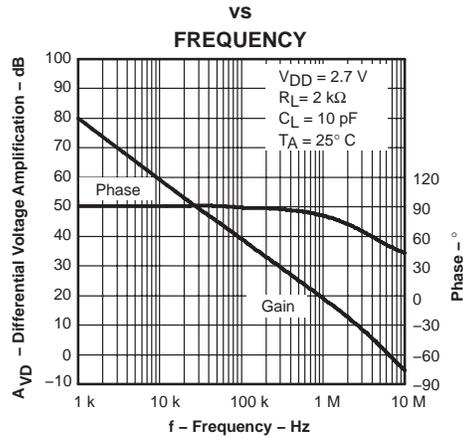


Figure 11

GAIN-BANDWIDTH PRODUCT

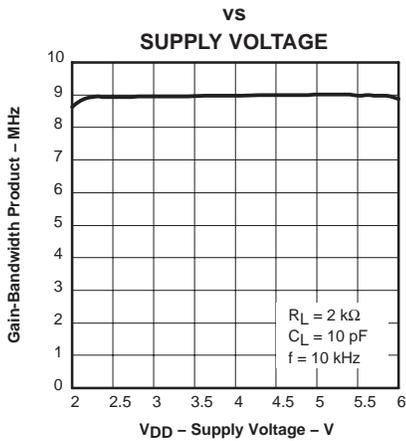


Figure 12

GAIN-BANDWIDTH PRODUCT

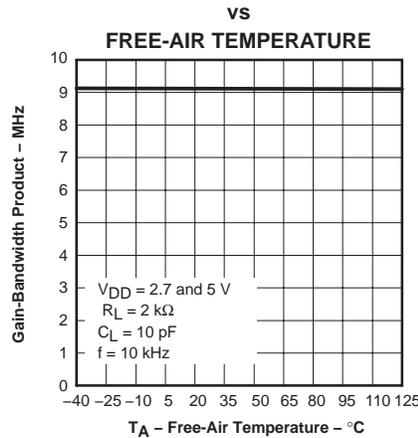


Figure 13

SLEW RATE

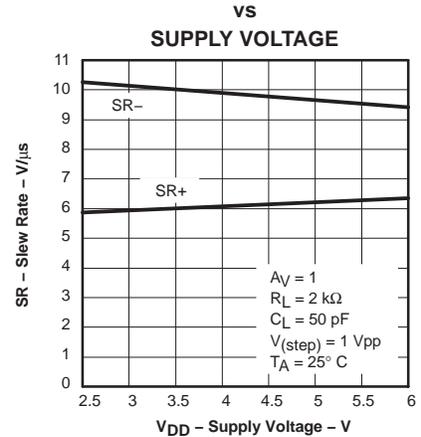


Figure 14

SLEW RATE

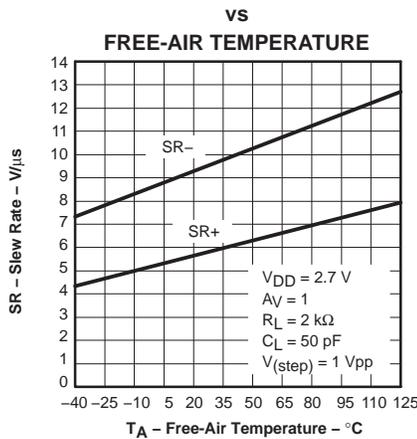


Figure 15

SLEW RATE

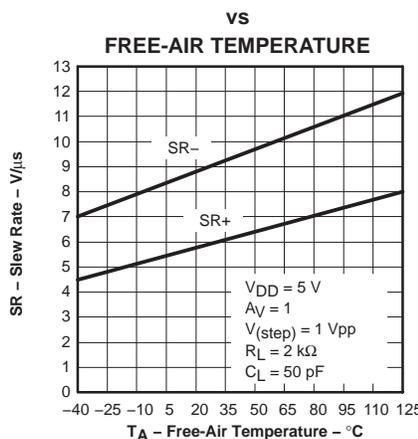


Figure 16

PHASE MARGIN

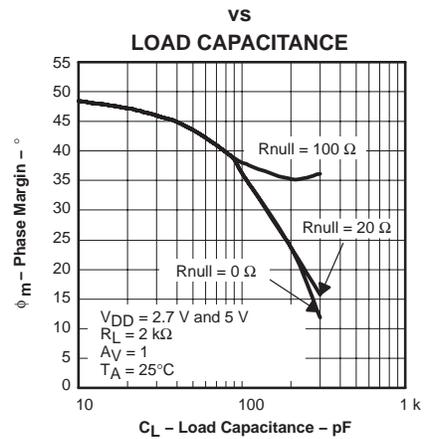


Figure 17

TYPICAL CHARACTERISTICS

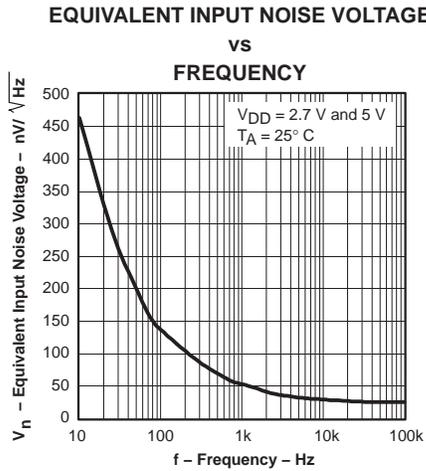


Figure 18

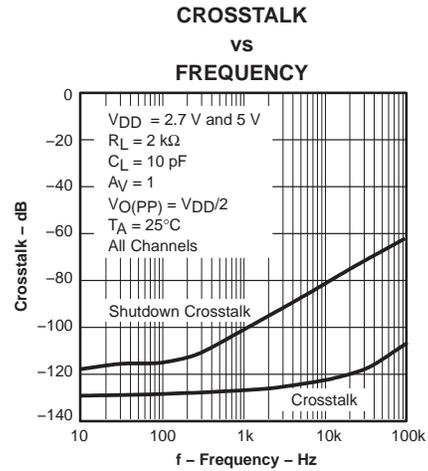


Figure 19

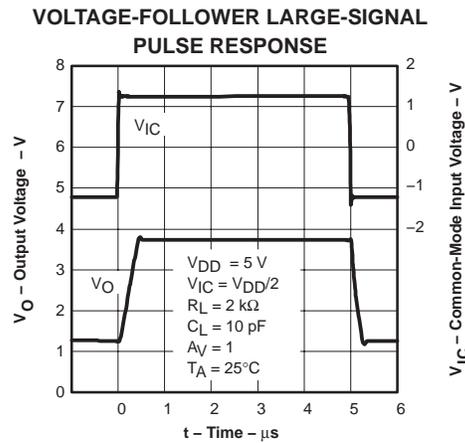


Figure 20

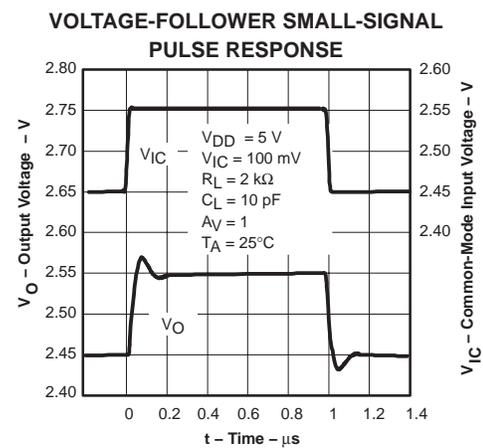


Figure 21

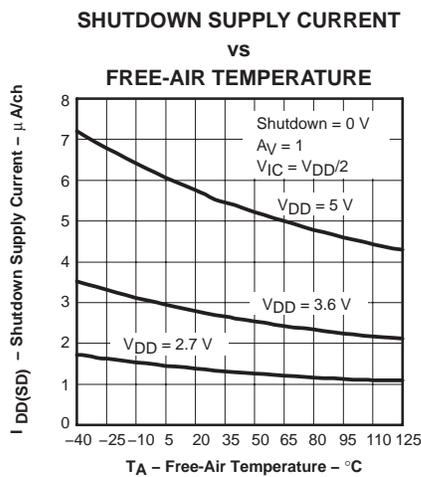


Figure 22

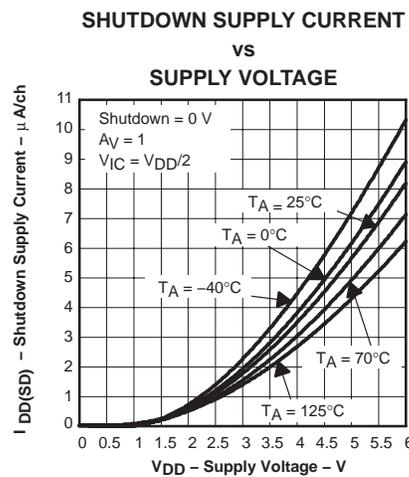


Figure 23

TYPICAL CHARACTERISTICS

SHUTDOWN SUPPLY CURRENT / OUTPUT VOLTAGE vs TIME

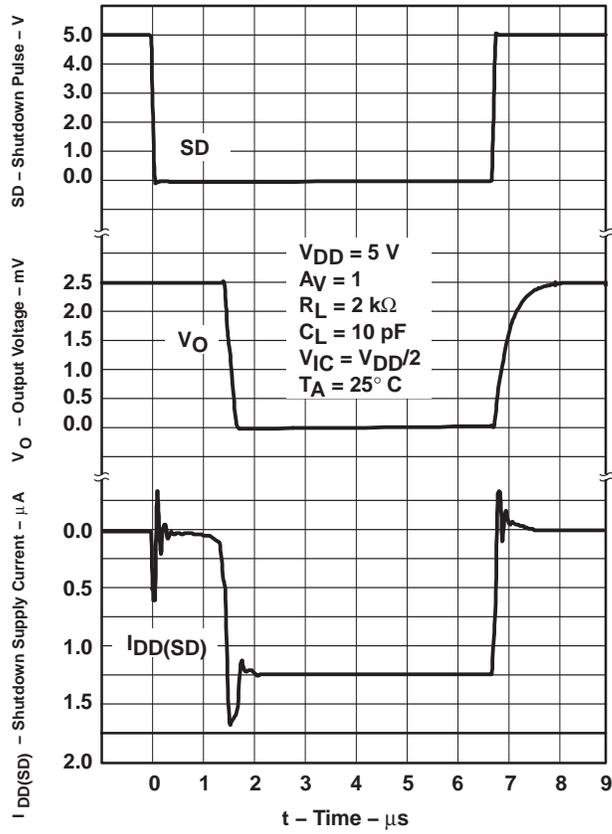
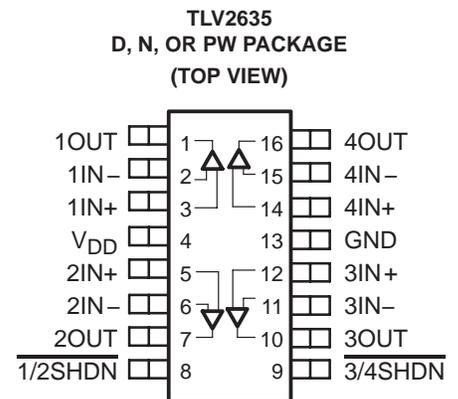
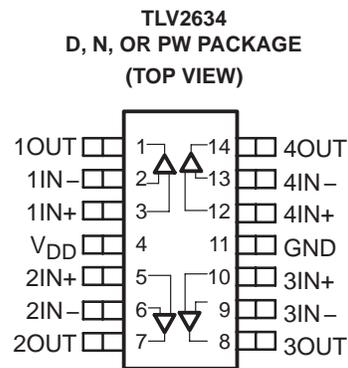
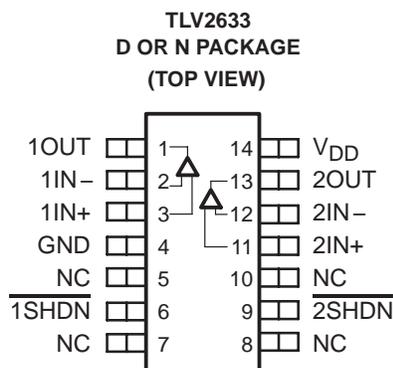
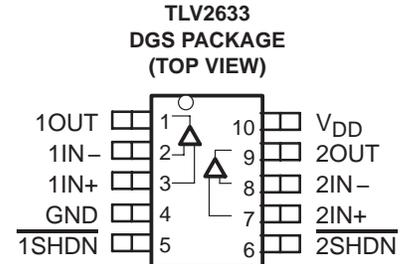
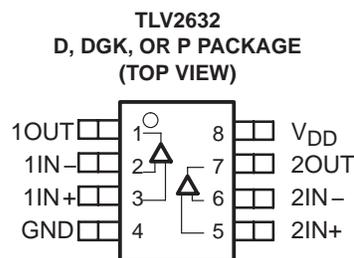
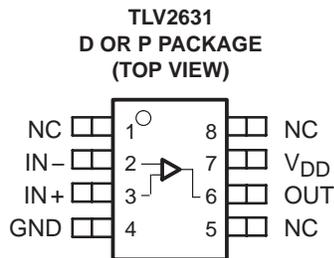
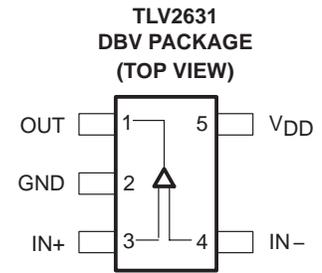
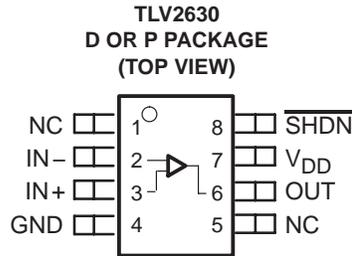
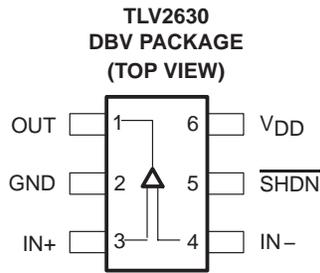


Figure 24

TLV263x PACKAGE PINOUTS



NC – No internal connection

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLV2630IDBVR	ACTIVE	SOT-23	DBV	6	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	VAYI	Samples
TLV2631IDBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	VAZI	Samples
TLV2631IDBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	VAZI	Samples
TLV2632IDGKR	ACTIVE	VSSOP	DGK	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	AKG	Samples
TLV2632IDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	2632I	Samples
TLV2634ID	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	2634I	Samples
TLV2634IPWR	ACTIVE	TSSOP	PW	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	2634I	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

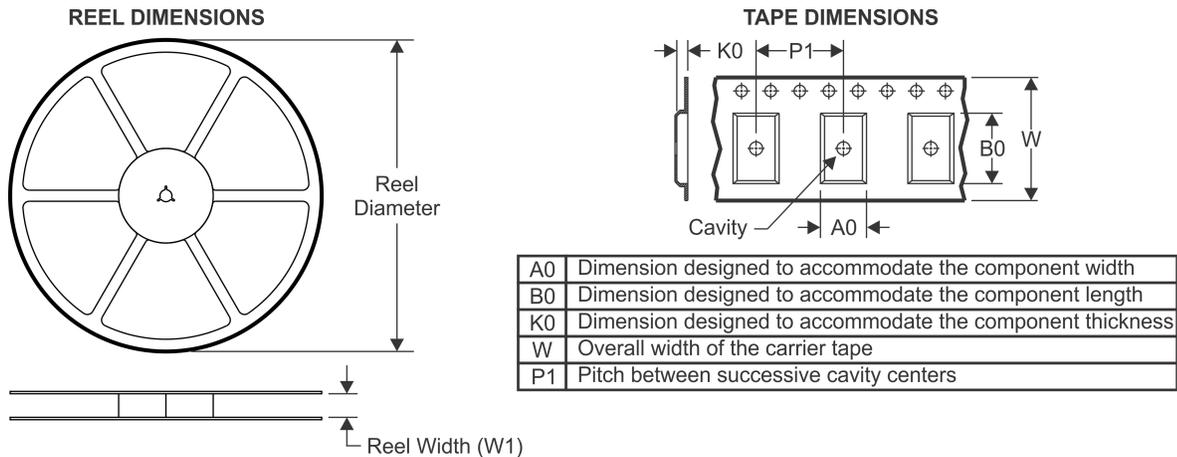
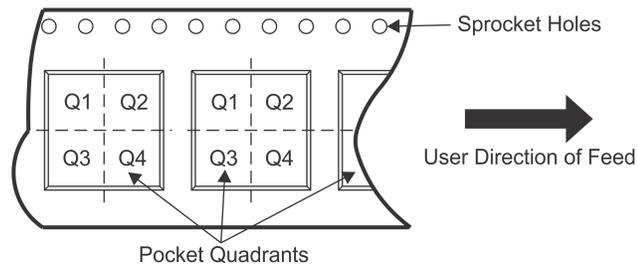
(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

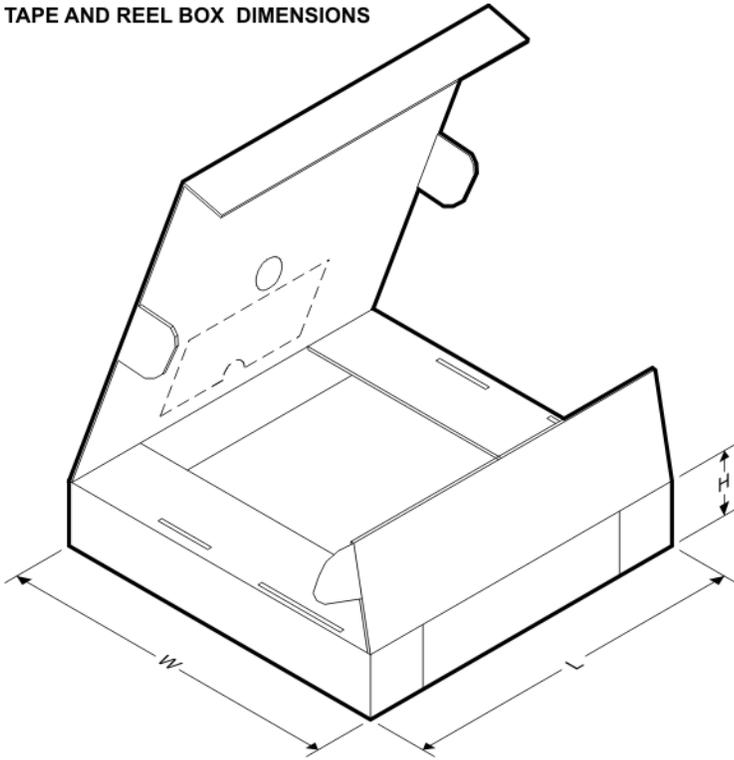
Important Information and Disclaimer:The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLV2630IDBVR	SOT-23	DBV	6	3000	180.0	9.0	3.15	3.2	1.4	4.0	8.0	Q3
TLV2631IDBVR	SOT-23	DBV	5	3000	180.0	9.0	3.15	3.2	1.4	4.0	8.0	Q3
TLV2631IDBVT	SOT-23	DBV	5	250	180.0	9.0	3.15	3.2	1.4	4.0	8.0	Q3
TLV2632IDGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
TLV2632IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLV2634IPWR	TSSOP	PW	14	2000	330.0	12.4	6.9	5.6	1.6	8.0	12.0	Q1

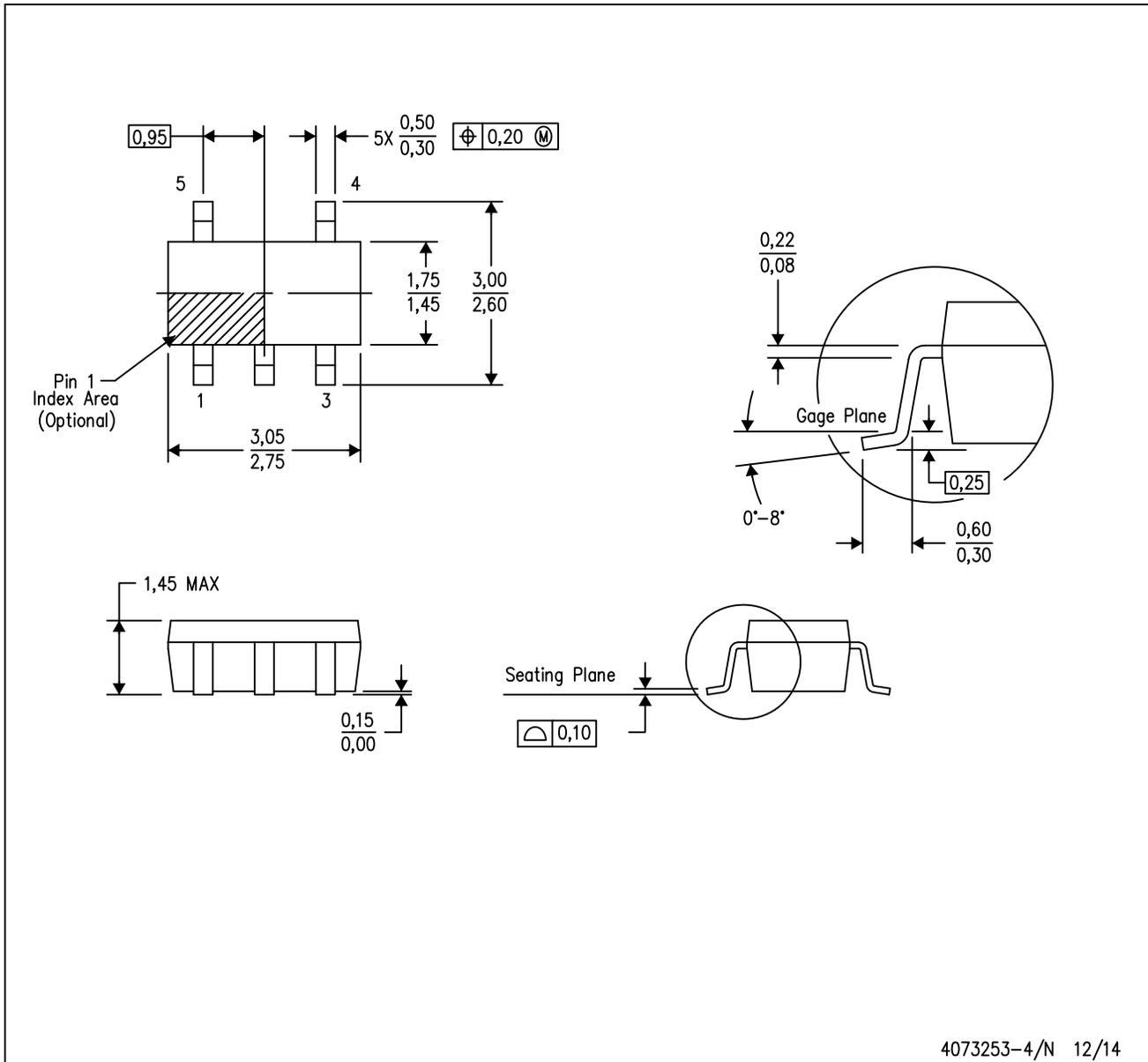
TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLV2630IDBVR	SOT-23	DBV	6	3000	182.0	182.0	20.0
TLV2631IDBVR	SOT-23	DBV	5	3000	182.0	182.0	20.0
TLV2631IDBVT	SOT-23	DBV	5	250	182.0	182.0	20.0
TLV2632IDGKR	VSSOP	DGK	8	2500	358.0	335.0	35.0
TLV2632IDR	SOIC	D	8	2500	340.5	338.1	20.6
TLV2634IPWR	TSSOP	PW	14	2000	367.0	367.0	35.0

DBV (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE

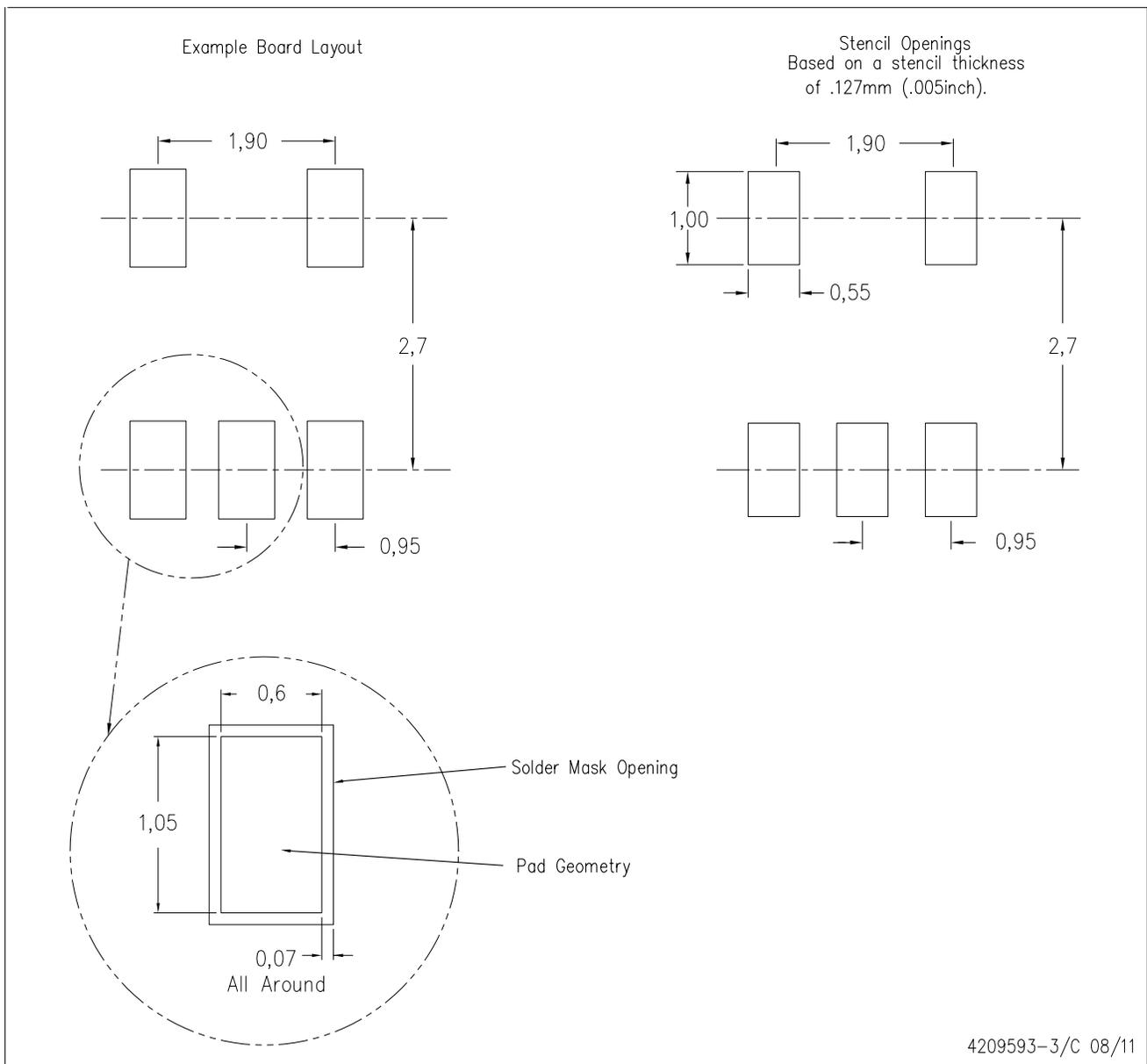


4073253-4/N 12/14

- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
 - Falls within JEDEC MO-178 Variation AA.

DBV (R-PDSO-G5)

PLASTIC SMALL OUTLINE

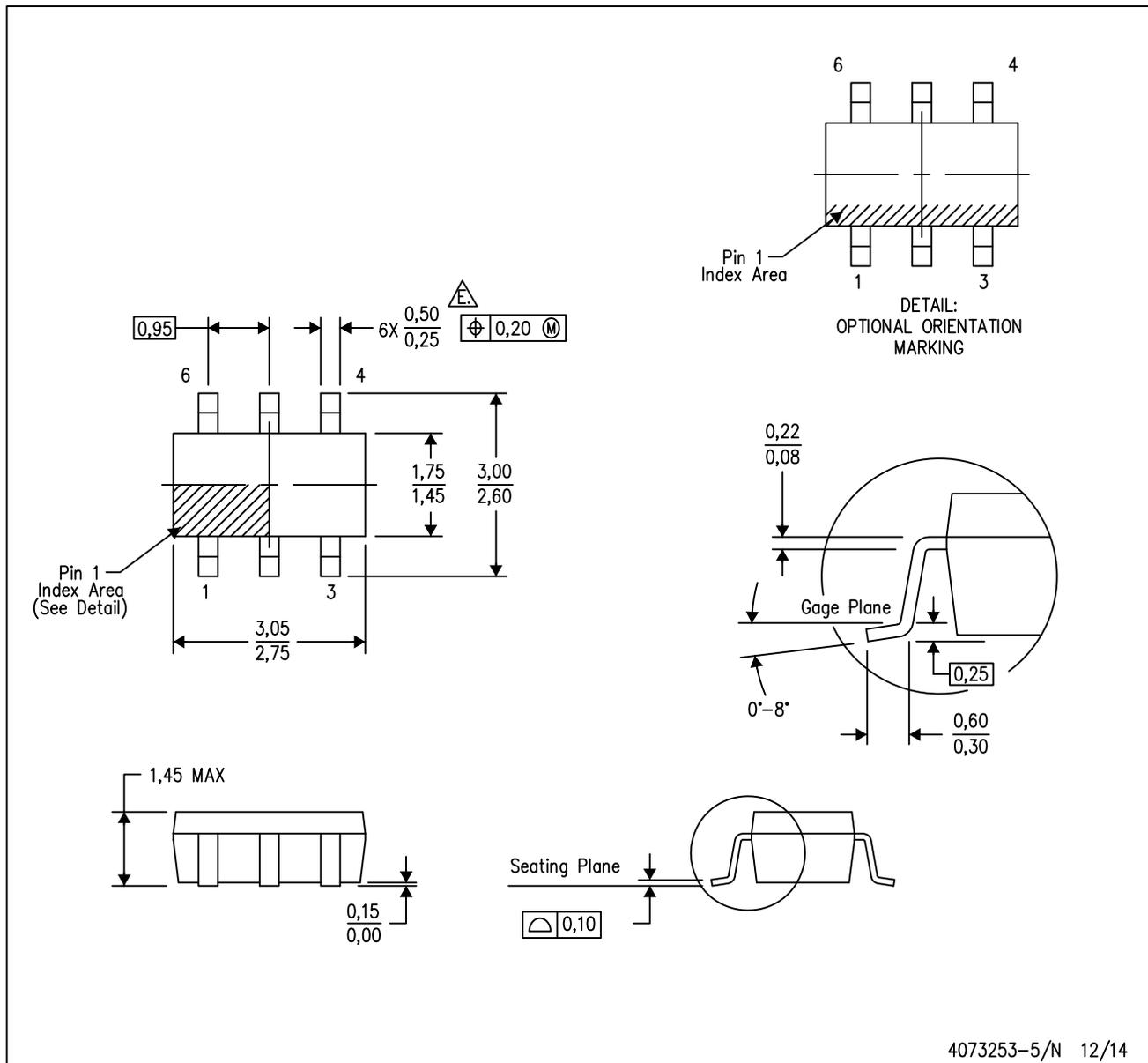


- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
 - D. Publication IPC-7351 is recommended for alternate designs.
 - E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.

MECHANICAL DATA

DBV (R-PDSO-G6)

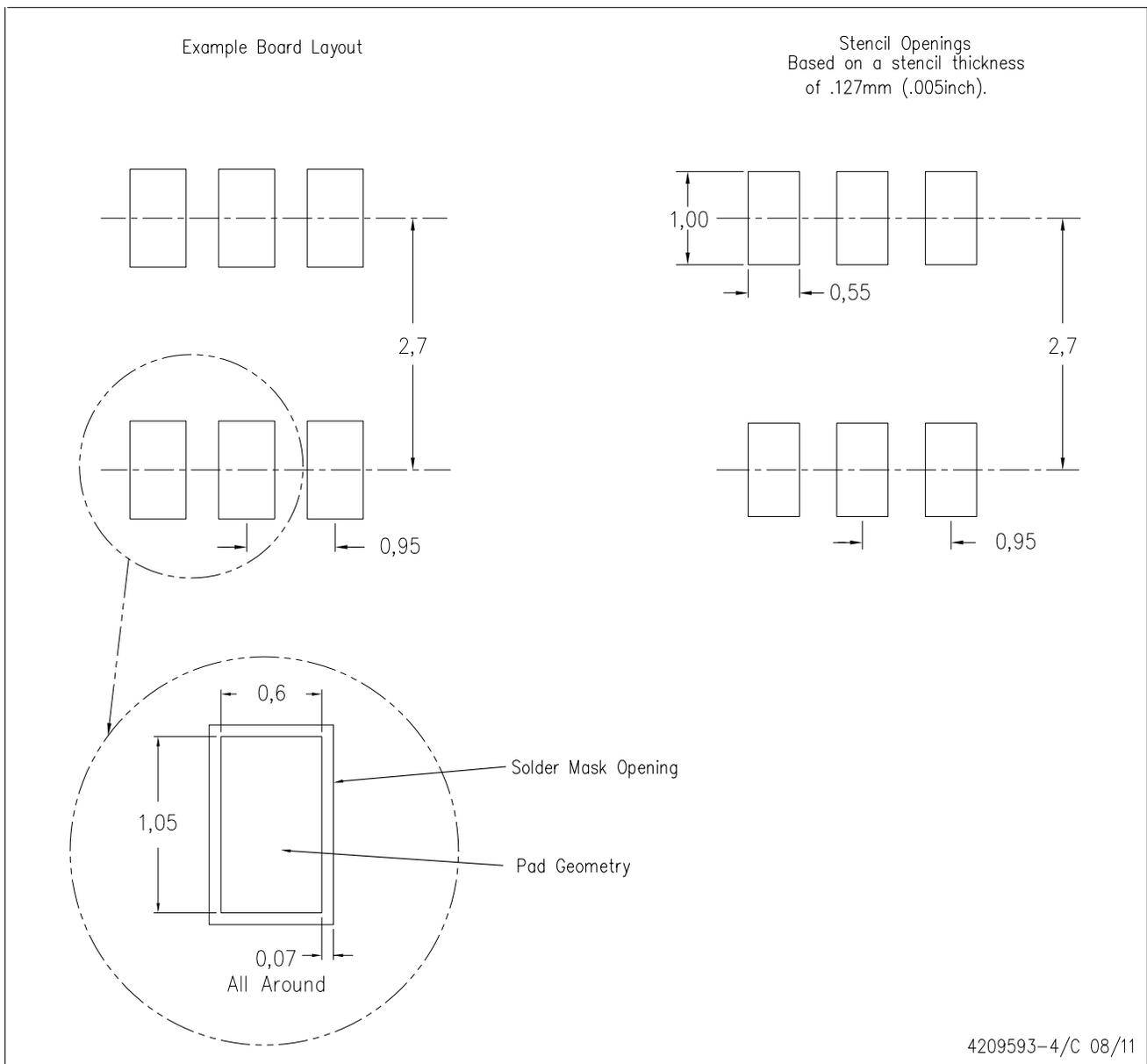
PLASTIC SMALL-OUTLINE PACKAGE



- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
 - Leads 1,2,3 may be wider than leads 4,5,6 for package orientation.
- Falls within JEDEC MO-178 Variation AB, except minimum lead width.

DBV (R-PDSO-G6)

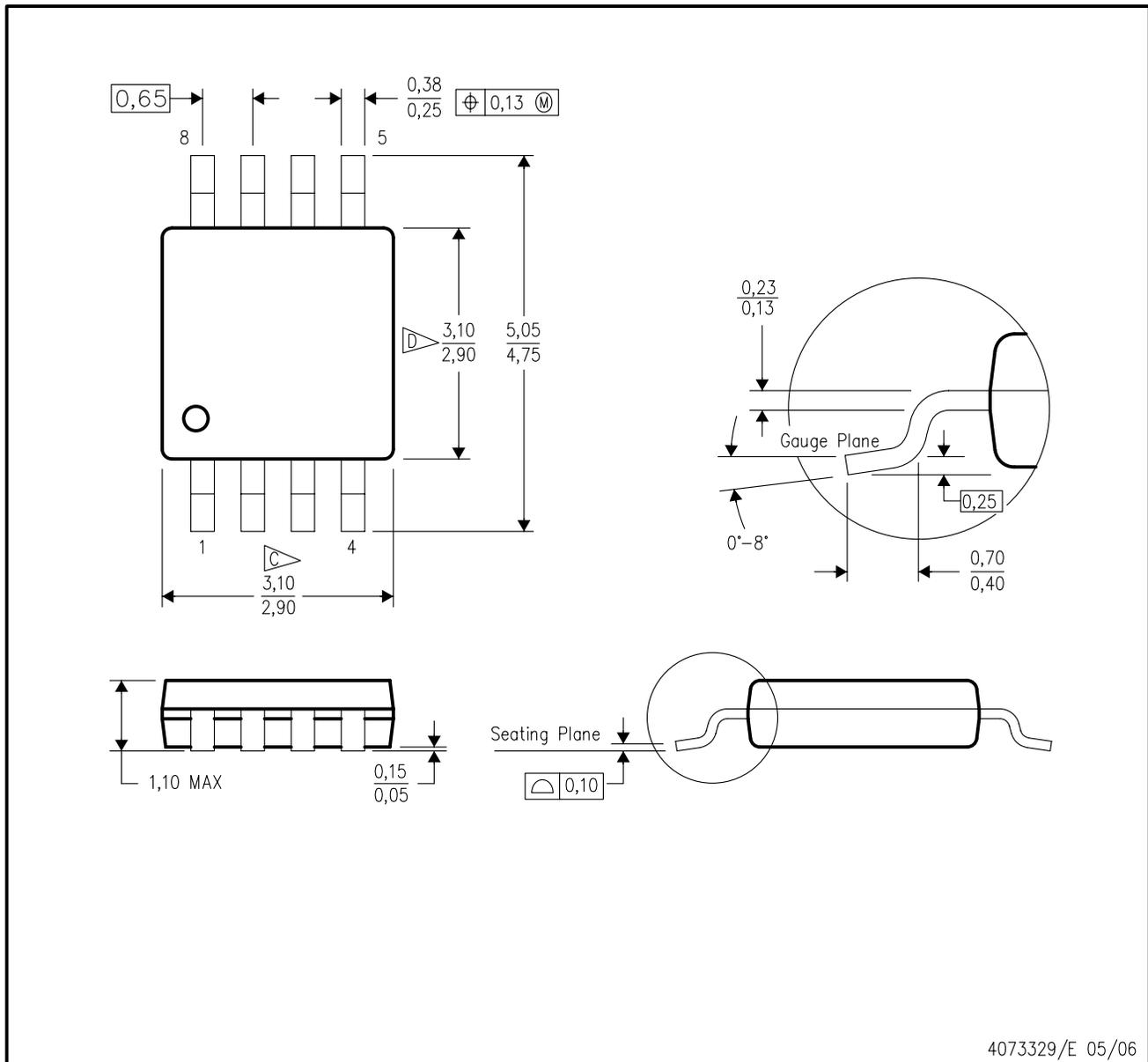
PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
 - D. Publication IPC-7351 is recommended for alternate designs.
 - E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.

DGK (S-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE

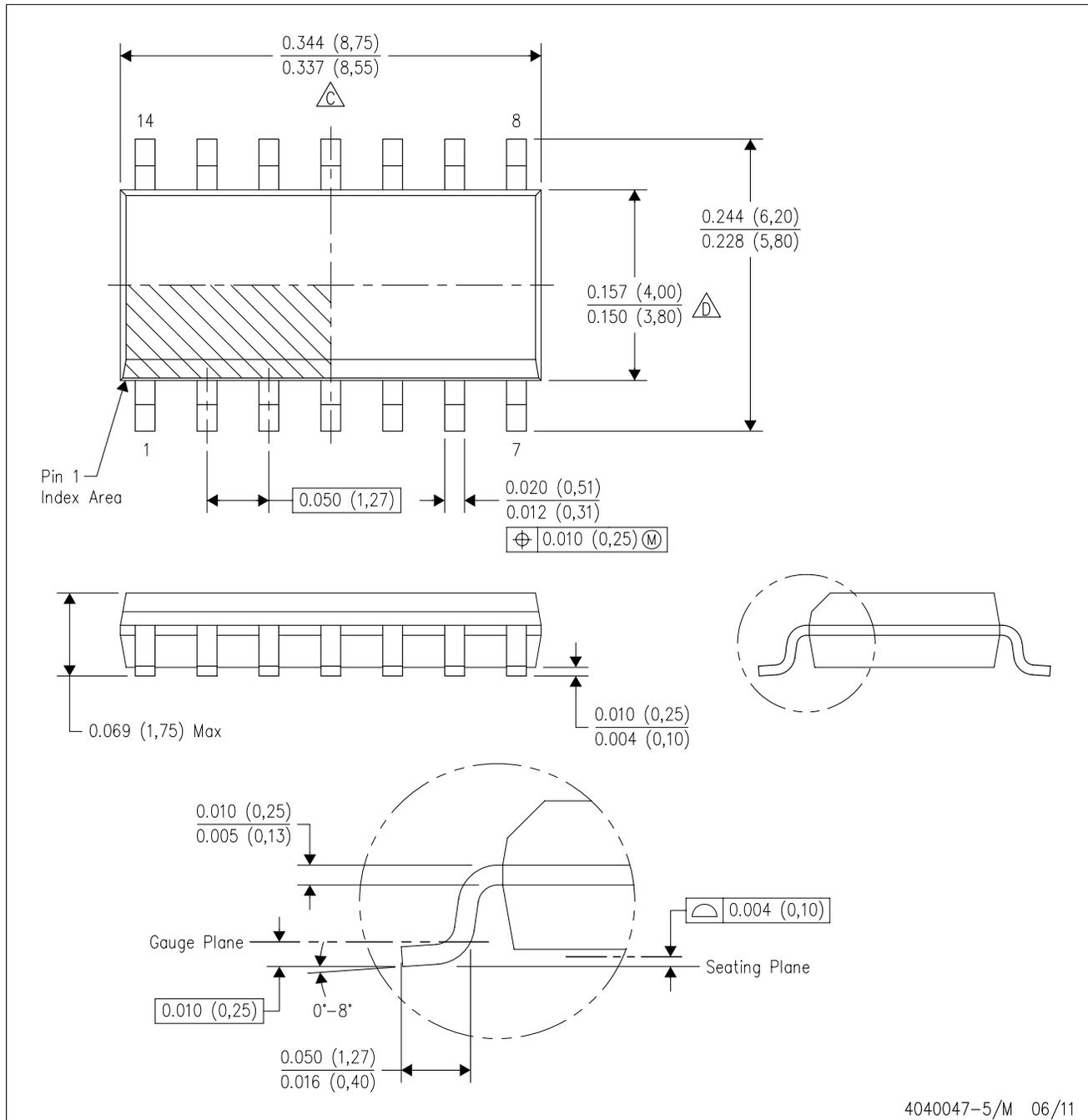


4073329/E 05/06

- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 per end.
 - D. Body width does not include interlead flash. Interlead flash shall not exceed 0.50 per side.
 - E. Falls within JEDEC MO-187 variation AA, except interlead flash.

D (R-PDSO-G14)

PLASTIC SMALL OUTLINE

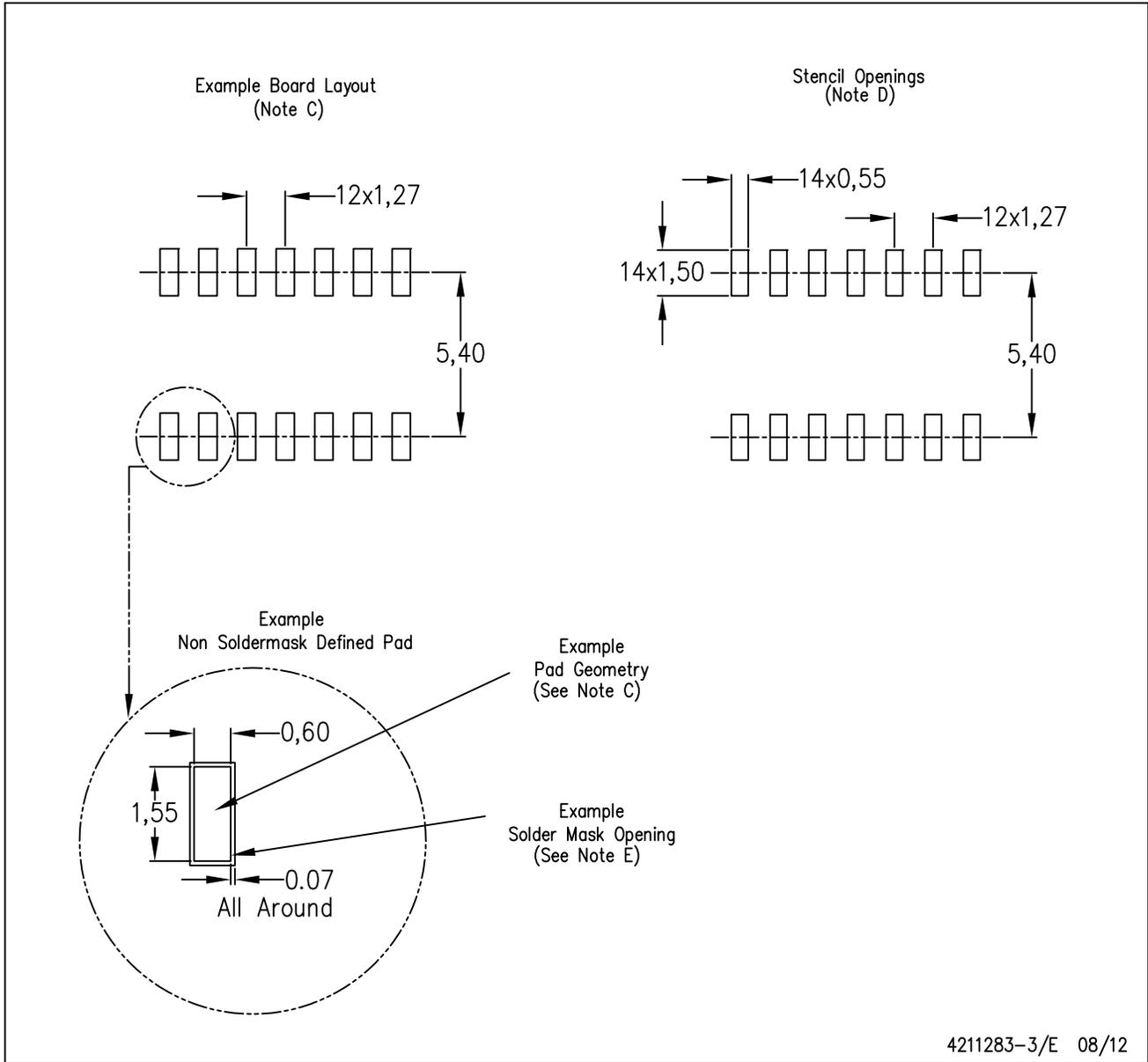


4040047-5/M 06/11

- NOTES:
- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 -  Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
 -  Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
 - E. Reference JEDEC MS-012 variation AB.

D (R-PDSO-G14)

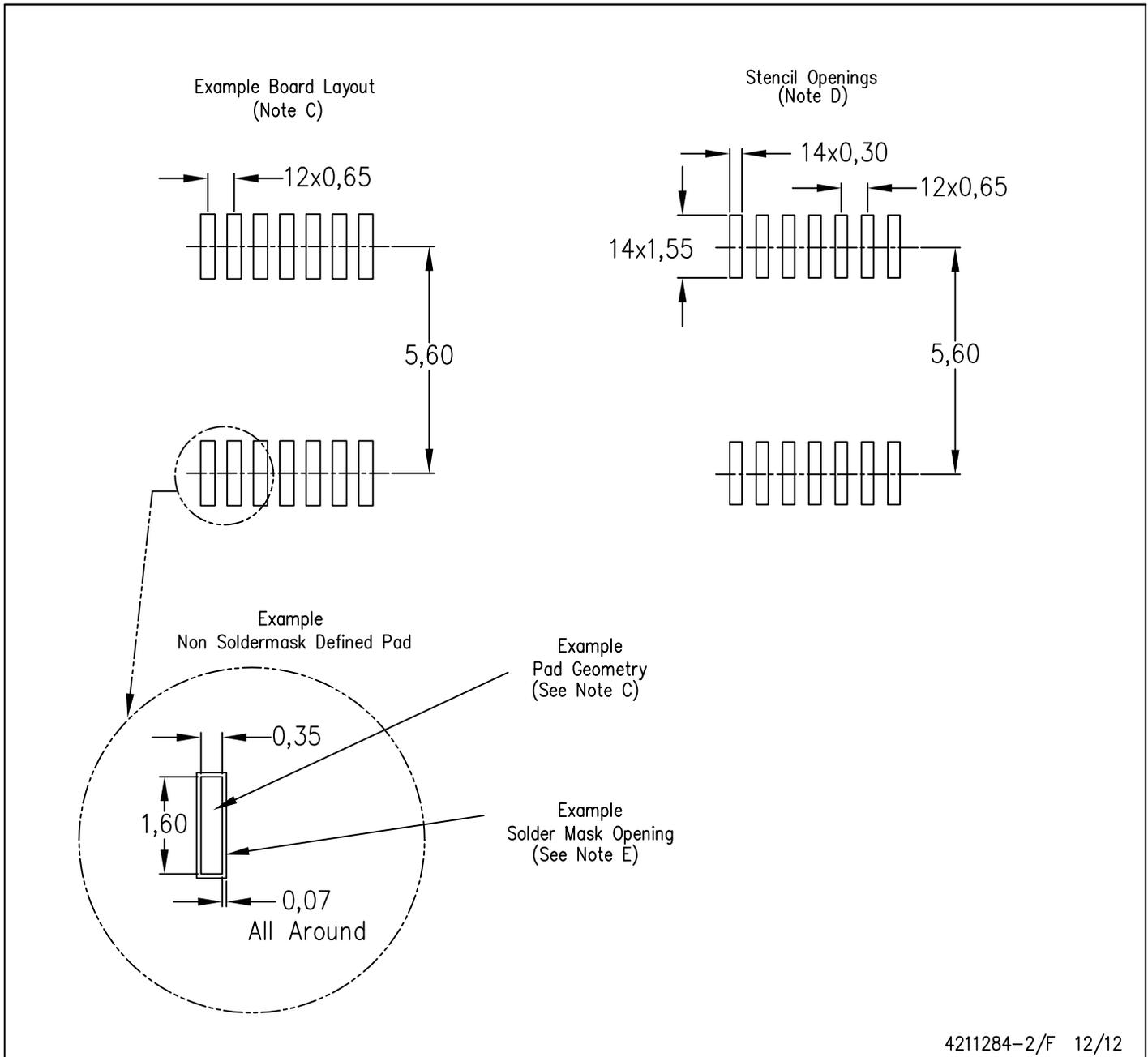
PLASTIC SMALL OUTLINE



- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Publication IPC-7351 is recommended for alternate designs.
 - Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
 - Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

PW (R-PDSO-G14)

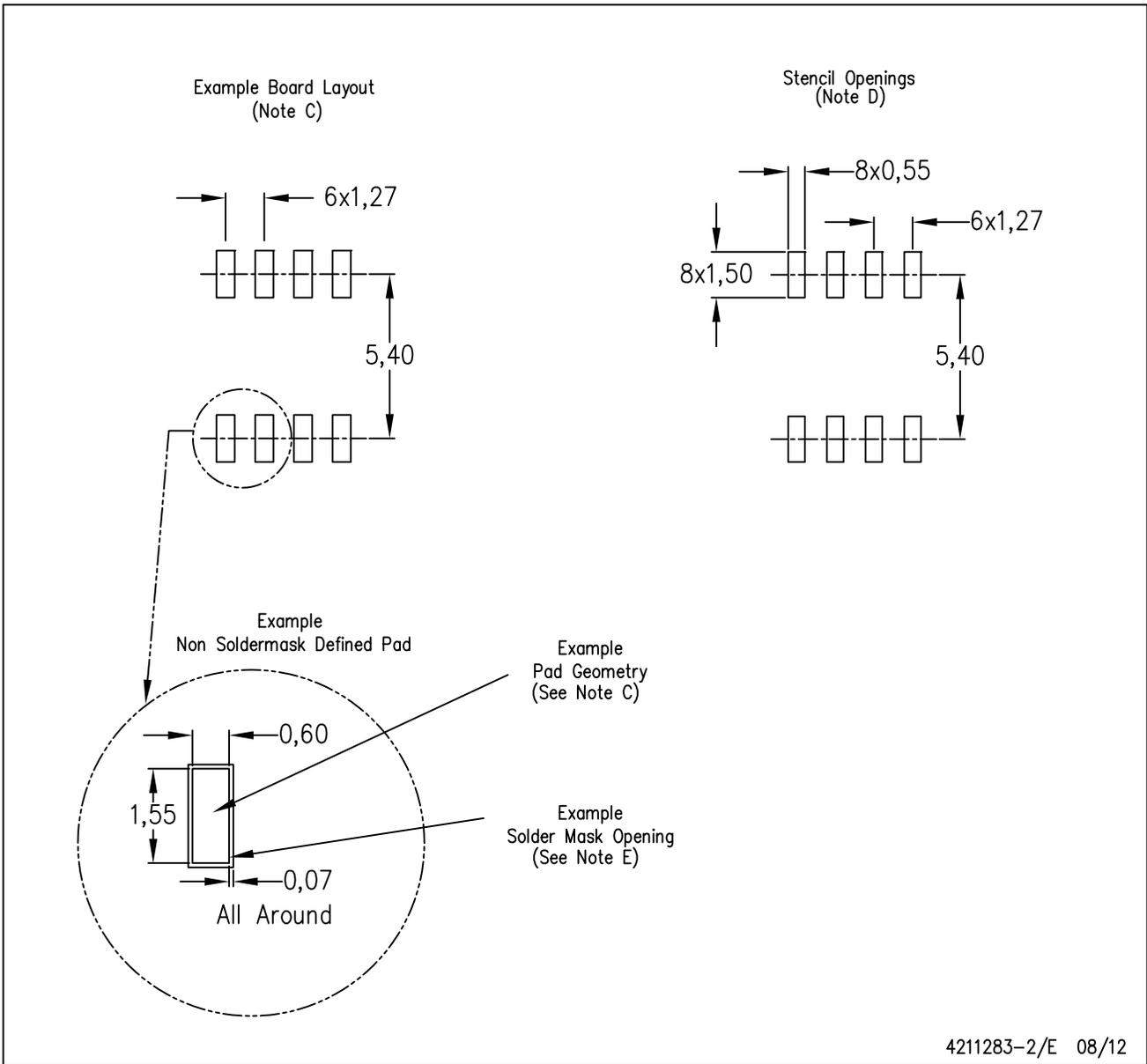
PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Publication IPC-7351 is recommended for alternate designs.
 - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
 - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

D (R-PDSO-G8)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Publication IPC-7351 is recommended for alternate designs.
 - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
 - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Products

Audio	www.ti.com/audio
Amplifiers	amplifier.ti.com
Data Converters	dataconverter.ti.com
DLP® Products	www.dlp.com
DSP	dsp.ti.com
Clocks and Timers	www.ti.com/clocks
Interface	interface.ti.com
Logic	logic.ti.com
Power Mgmt	power.ti.com
Microcontrollers	microcontroller.ti.com
RFID	www.ti-rfid.com
OMAP Applications Processors	www.ti.com/omap
Wireless Connectivity	www.ti.com/wirelessconnectivity

Applications

Automotive and Transportation	www.ti.com/automotive
Communications and Telecom	www.ti.com/communications
Computers and Peripherals	www.ti.com/computers
Consumer Electronics	www.ti.com/consumer-apps
Energy and Lighting	www.ti.com/energy
Industrial	www.ti.com/industrial
Medical	www.ti.com/medical
Security	www.ti.com/security
Space, Avionics and Defense	www.ti.com/space-avionics-defense
Video and Imaging	www.ti.com/video

TI E2E Community

e2e.ti.com